

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 04-171860
(43)Date of publication of application : 19.06.1992

(51)Int.Cl. H01L 27/10
G03F 1/08
H01L 21/027

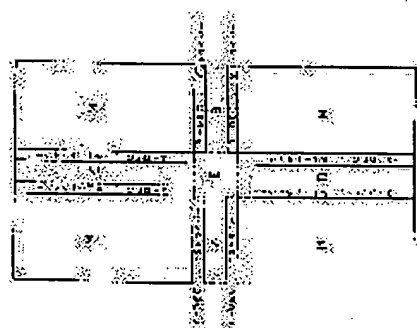
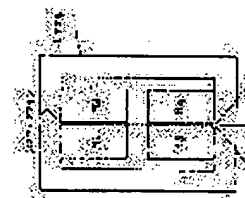
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(54) MANUFACTURE OF SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND RETICLE USED FOR IT

(57)Abstract:

PURPOSE: To manufacture a circuit pattern composed of minute elements in an exposure region with a large resolution margin, by manufacturing a reticle arranged so that the section of a circuit block composed of minute elements put on a chip or bridging adjacent two chips on a semiconductor wafer may be the center section, or a mask formed on the basis of the reticle, and by manufacturing the circuit pattern on the semiconductor wafer using it.

CONSTITUTION: A circuit pattern to form the right half of a DRAM is made in the left half of a reticle, and a circuit pattern to form the left half of the DRAM is made in the right half of the reticle, so that a scribed line by a dotted line dividing adjacent two chips may be in the vertical center. Memory arrays M1 and M3 are those which constitute right-hand side memory arrays in their original DRAMs, and in their left-hand side periphery circuit patterns corresponding to the right halves of areas C, E and D are made. Whereas, memory arrays M2 and M4 are those which constitute left-hand side memory arrays in their original DRAMs, and in their right-hand side periphery circuit patterns corresponding to the left halves of the areas C, E and D are made.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

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